

# Search history

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L24	4943	semiconductor and integrated and circuit and input/output and (high same frequency) and channel\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/03 04:53
L25	536	24 and (first same (external same terminal\$1))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/03 04:47
L26	73	25 and (second same (external same terminal\$1)) and (higher same impedance)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/03 04:48
L27	71	26 and (first same (between same second))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/03 04:48
L28	66	27 and capacitor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/03 04:49
L29	50	28 and (capacitor same (one near end))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/03 04:49
L30	0	29 and (capacitor same (other near end))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/03 04:50
L31	48	29 and (predetermined same (potential or voltage))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/03 04:50

L32	1	31 and "365"/\$7.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/03 04:50
L33	48	31 and (high same (frequency with signal\$1))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/03 04:51
L34	48	33 and (first same terminal\$1) and (second same terminal\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/03 04:51
L35	6	34 and (plurality same terminals)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/03 04:54
L36	0	24 and naosumi-waki.in.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/03 04:53
L37	18548	semiconductor and integrated and circuit and input and output and (high same frequency) and channel\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/03 04:53
L38	0	36 and naosumi-waki.in.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/03 04:53
L39	6	34 and (plurality same (terminals or pins))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/03 04:54

L40	1360	24 and (plurality same (terminals or pins))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/03 04:54
L41	99	40 and (higher same impedance)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/03 04:54
L42	67	41 and capacitor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/03 04:54
L43	2	42 and "365"/\$7.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/03 04:55
L44	63099	semiconductor and (plurality same (pins or terminals))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/03 04:55
L45	41170	44 and (first same (terminal or pin))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/03 04:56
L46	35890	45 and (second same (terminal or pin))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/03 04:56
L47	1694	46 and (higher adj frequency)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/03 04:56

L48	1125	47 and capacitor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/03 04:56
L49	0	48 and (one near end) and (other near end)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/03 04:57
L50	443	48 and (one near end)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/03 04:57
L51	104	50 and (predetermined same potential)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/03 04:58
L55	1	44 and waki-naosumi.in.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/03 05:06
L56	1	(semiconductor and input/output and (high adj frequency) and channels and (first near (external terminal)) and (second near (external terminal)) and (higher impedance) and capacitor and (one end) and (other end) and predetermined).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/03 05:08